

Lecture 4: Translating the Meaning of Safety

CS350

Marco Patrignani

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- Safety property in S : **never output false**
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- timing is key to failure here

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Q1: how do we bridge this gap?

- **A:** we can add extra actions to the target alphabet (one for **t**) (otherwise no correct compilation is possible)
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Q1: how do we bridge this gap?

Q2: how do we **preserve the meaning** of properties across languages?

(otherwise no correct compilation is possible)

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Different Trace Alphabets

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Recommended reading:

Patrignani and Garg. 2017. Secure Compilation as Hyperproperty Preservation.

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Cross-language Relation

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- What does this mean?
- We only know for safety and hypersafety

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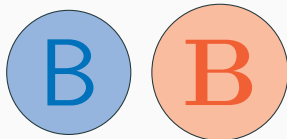
dually

Safety Preservation with TPC

- safety = traces where something bad does not happen

dually

- safety = set of bad prefixes



Safety, Visually



Safety, Visually

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Extra Target Actions

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- Good η : outgoing action (!) respect safety,

- Bad η : outgoing action (!) violate safety
- We have no control over η ?
But if we only use \checkmark for $\eta!$ we can
preserve safety and hypersafety

outgoing action (!), violate safety

Recall

- π is a property: $\{t\}$
- if π is safety, we can express it dually via the set of bad prefixes $\{m\}$ such that $\{m\} :: \pi$
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- H is a hyperproperty: $\{\pi\}, \{\{t\}\}$
- if H is hypersafety we can express it dually via the set of sets of bad prefixes $\{\{m\}\}$ such that $\{\{m\}\} :: H$
- compact notation: $\{\{m\}\} = \mathbb{M}$

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Singletons: **minimum addition** s.t. any **P** not responding even once as \checkmark are bad

NI stays NI

Q: How do we know that \mathbb{S}^{HP} is correct?

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Q: How do we know that SHP is correct?

We prove something!

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Q: How do we know that $\overset{\text{SHP}}{\approx}$ is correct?

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Theorem (Non-interference is preserved)

Let $M :: NI$. Let $M \overset{\text{SHP}}{\approx} M$ and let S be a hyperproperty such that $M :: S$. Then,
 $\forall t \in S, t \in NI$.

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- Can we devise a compiler that preserves hypersafety across languages?
- How will it differ from RSC / RHS ?

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With these assumptions:

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- reactive setting: only I/O traces (implicitly robust)
- traces capture all form of behaviour

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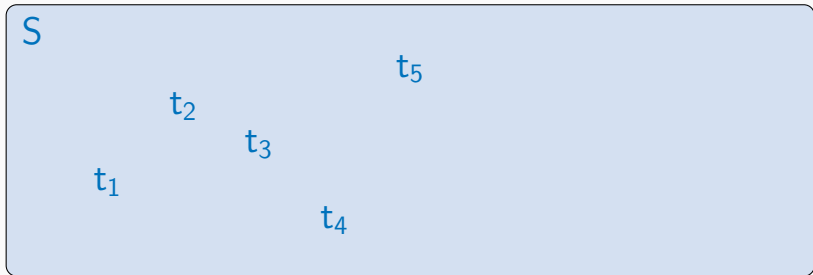
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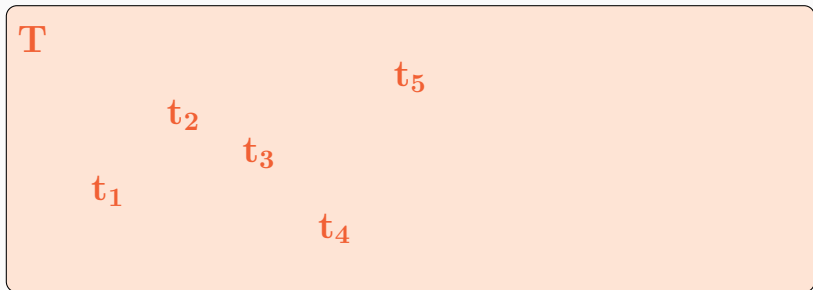
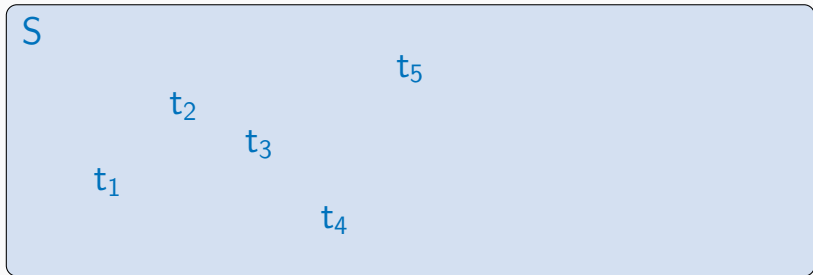
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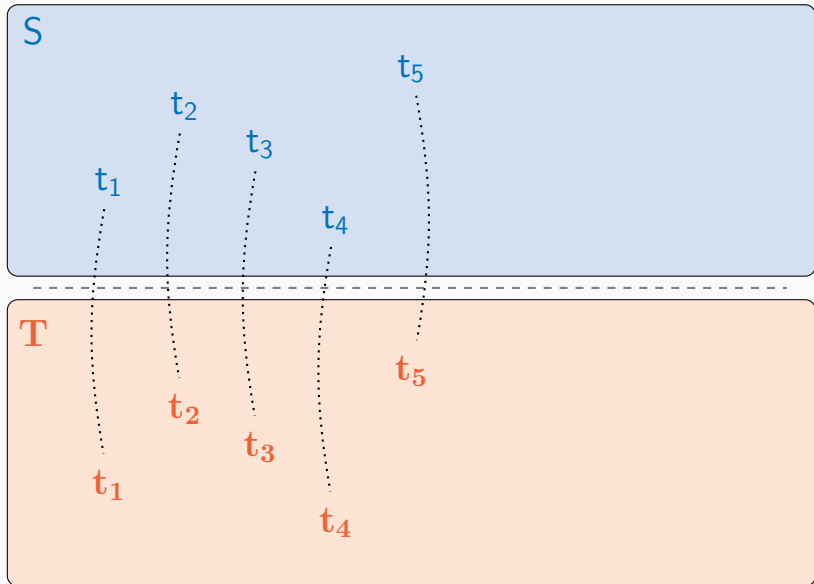
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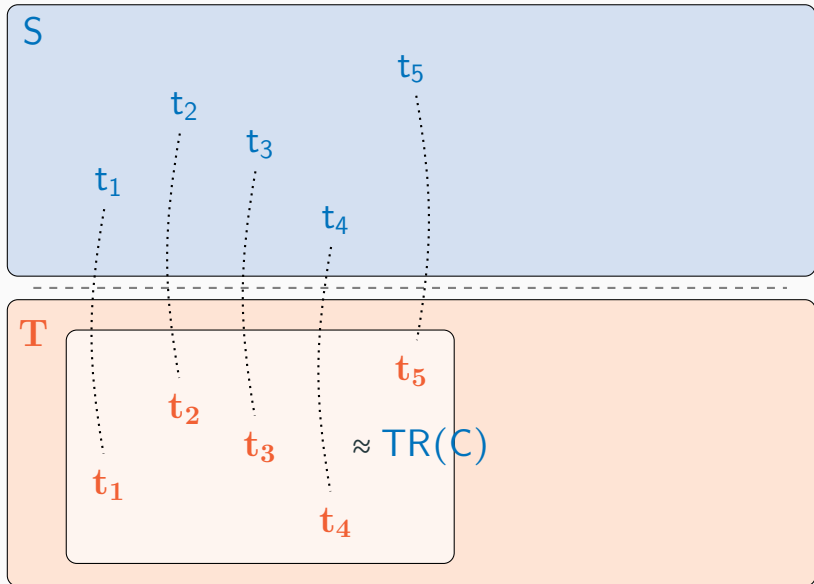
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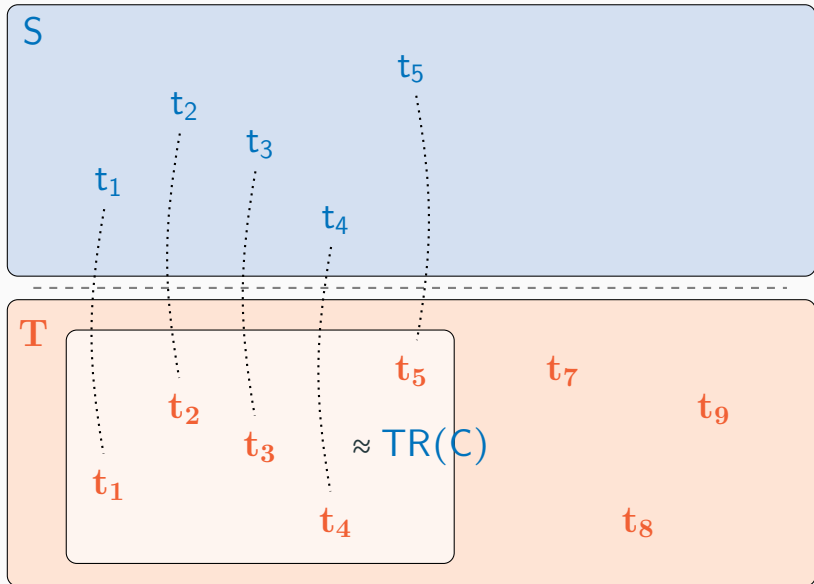
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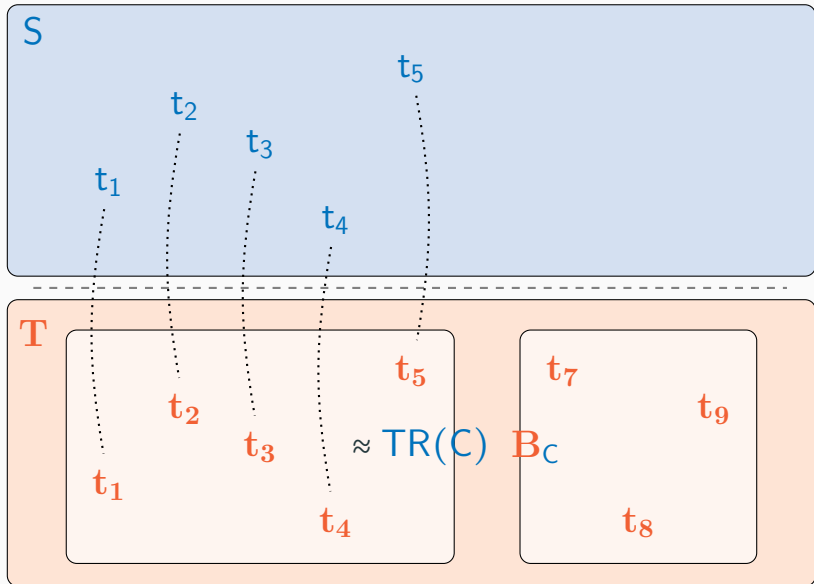
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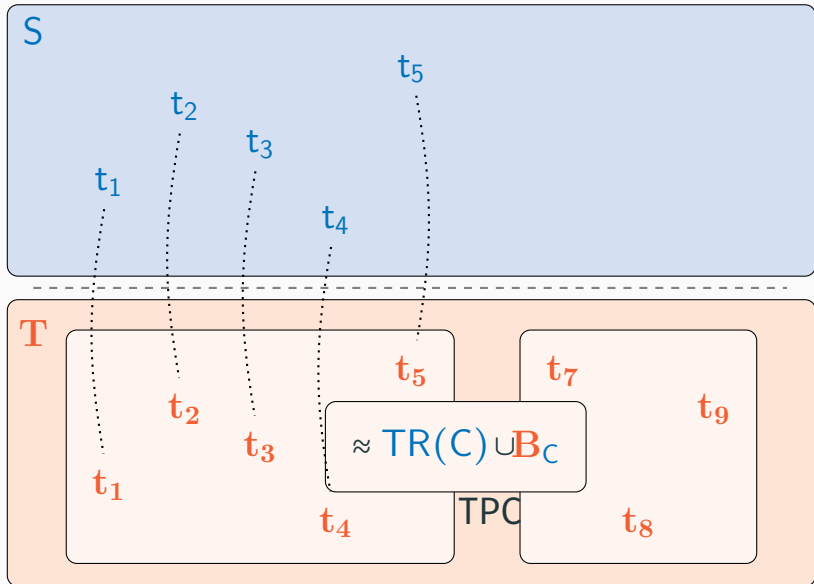
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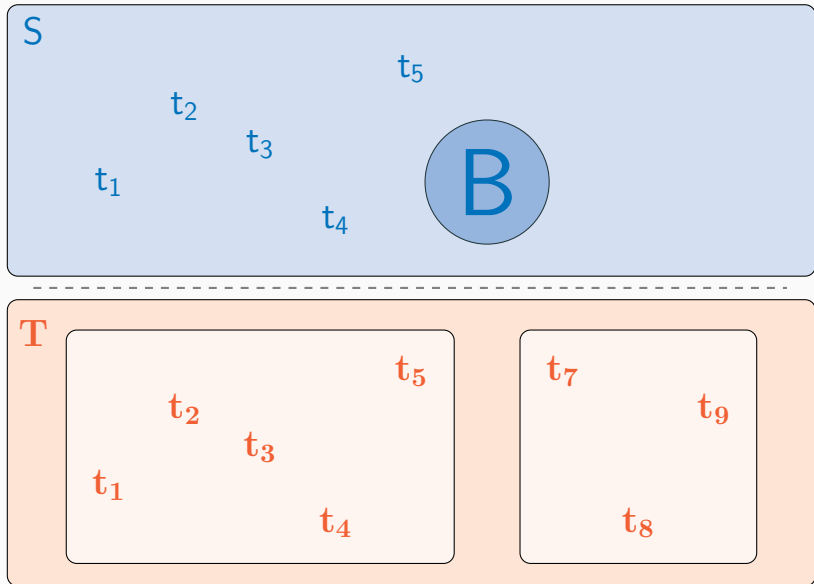
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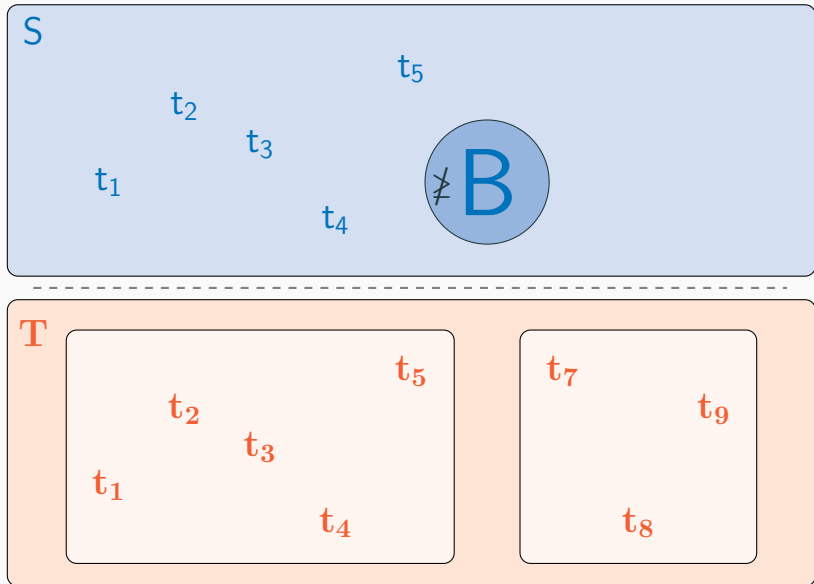
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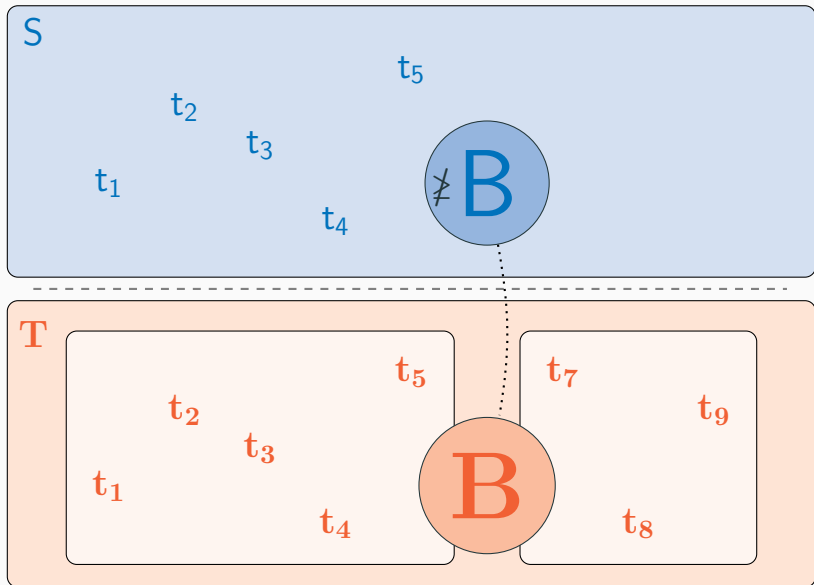
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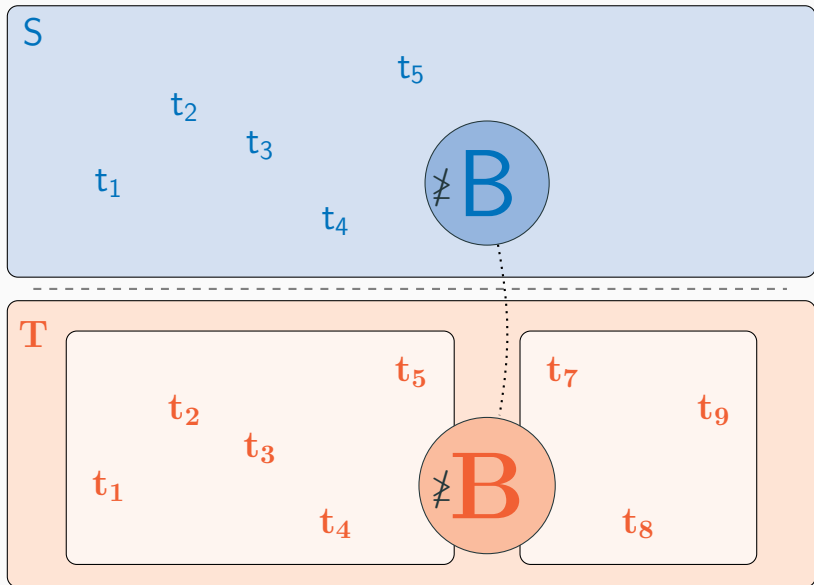
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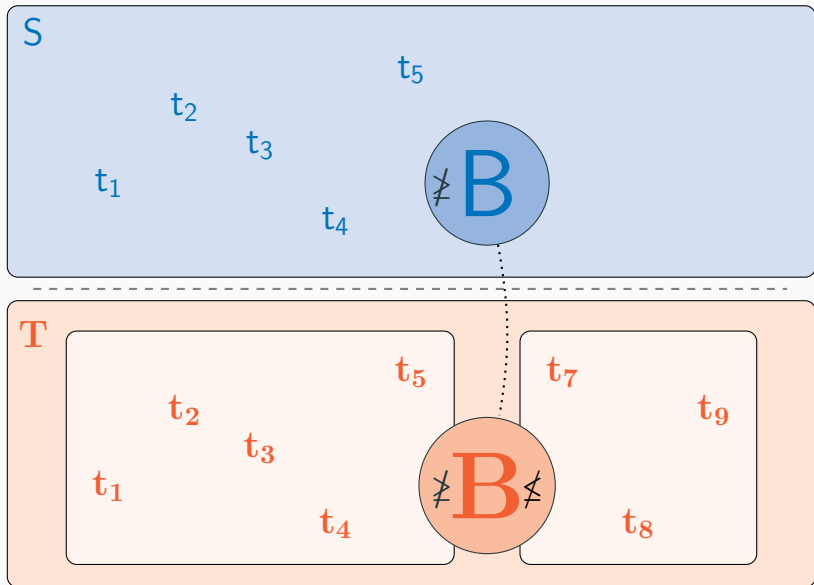
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- all are **robust** (TPC implicitly quantifies over all \mathcal{C})
- RC criteria (except for RSC and RHS) deal with properties
- TPC cannot handle properties