# The DRF theorem

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## Motivation for the DRF theorem

## WMC is complicated:

- Most programmers "do not understand" WMC.
- ▶ Leads to subtle bugs ~> hard to debug and fix.

## Define programming disciplines that:

- Avoid weak behaviors.
- Can be understood without referring to the WMM.

### The DRF discipline:

- Do not have any data races.
- Just use locks for synchronization.

### Definition (DRF property)

A memory model X satisfies the *DRF property* if for every program that is race-free under SC semantics, its allowed outcomes under X are the same as under SC.

- A programming discipline to avoid weak behavior.
- ► The premise requires us to establish race-freedom *under* SC.
- So a defensive programmer does not need to understand WMM.

For specific memory models, one can establish more permissive programming disciplines that ensure the absence of weak behaviors.

## What models satisfy the DRF property?

Among the models we saw so far, which satisfy the DRF property?

- COH
- StrongCOH
- RA
- ► C11
- ► TSO

The DRF property can be also taken as a definition of a "catch fire" crude model:

- If the program is race-free under SC, then the allowed outcomes are the same as under SC
- Otherwise, "undefined behavior" (*i.e.*, any outcome is allowed!)

## What constitutes a race under SC? (operationally)

#### Definition (racy program under SC (operationally))

*P* is called *racy under* SC if there exist P', S', M' such that the following hold:

$$\blacktriangleright P, S_0, M_0 \Rightarrow^* P', S', M'$$

▶  $P', S' \xrightarrow{i_1:l_1}$  and  $P', S' \xrightarrow{i_2:l_2}$  for some  $i_1 \neq i_2$ , and labels  $l_1$  and  $l_2$ , such that  $loc(l_1) = loc(l_2)$ , and  $\{typ(l_1), typ(l_2)\} \cap \{W, RMW\} \neq \emptyset$ 

## What constitutes a race under SC? (declaratively)

#### Definition (race)

Given an execution graph G and a relation  $R \subseteq G.E \times G.E$ , we say that two events a, b *R*-race in G if the following hold:

- a ≠ b
- ▶ loc(a) = loc(b)
- $\{\texttt{typ}(a),\texttt{typ}(b)\} \cap \{\texttt{W},\texttt{RMW}\} \neq \emptyset$
- $\langle a, b \rangle \not\in R^+$  and  $\langle b, a \rangle \notin R^+$

#### Definition (racy execution)

An execution graph G is called R-racy if there are two events that R-race in G.

#### Definition (racy program under SC (declaratively))

P is called *racy under* SC if there exists an execution graph G such that the following hold:

- G is a  $(po \cup rf)^+$ -prefix of an execution of P
- G is SC-consistent
- G is (po  $\cup$  rf)-racy

#### What constitutes a race under SC?

The two definitions differ for programs with RMW's:

(FAI(y) is an atomic fetch-and-increment)

- Operational definition: the program is racy under SC
- Declarative definition: the program is not racy under SC
- Declaratively racy under SC  $\Rightarrow$  operationally racy under SC
- ► For programs without RMW's, the definitions coincide.
- Next, for simplicity, we assume the declarative definition (and restrict RMW's when needed).

## The DRF property

$$\begin{array}{c|c} a := x; & //1 \\ \text{if } a \text{ then} \\ y := 1 \end{array} \quad \begin{array}{c|c} b := y; & //1 \\ \text{if } b \text{ then} \\ x := 1 \end{array}$$

- ✓ StrongCOH
- 🗸 RA
- X C11: same reason as for COH (using rlx accesses)
- ✓ RC11 (C11 with  $(po \cup rf)$  acyclicity)
- 🖌 TSO

## Proving DRF for RA

To prove that RA satisfies the DRF property, we have:

1. The easy part of the proof:

# Lemma If an RA-consistent execution graph G contains no $(po \cup rf)$ -races, then it is also SC-consistent.

2. The more difficult part:

#### Lemma

If P has an RA-consistent  $(po \cup rf)$ -racy execution graph, then P is racy under SC.

We prove the latter by considering the "first" race of the execution.

# Proof outline (1)

- Let G be a the an RA-consistent (po ∪ rf)-racy execution graph of P.
- Let G' be a minimal (po∪rf)-prefix of G that is (po∪rf)-racy.
- ▶ NB: This prefix might not be unique (*e.g.*, SB).
- Let a, b be two events that  $(po \cup rf)$ -race in G'.

- ▶ G' is RA-consistent. (why?)
- Let  $G'' \triangleq G' \setminus \{a, b\}.$ 
  - ▶ G" is RA-consistent.
  - G'' is not (po  $\cup$  rf)-racy.

Therefore, G'' is SC-consistent.

# Proof outline (2)

#### Possible cases:

• 
$$typ(a) = W$$
 and  $typ(b) = W$ 

- ▶  $ext{typ}(a) \in \{ ext{R}, ext{RMW} \}$  and  $ext{typ}(b) = ext{W}$
- ▶ typ(a) = W and  $typ(b) \in \{R, RMW\}$  (symmetric)
- typ(a) = R and typ(b) = RMW
- typ(a) = RMW and typ(b) = R (symmetric)
- We cannot have typ(a) = RMW and typ(b) = RMW. (why?)

CASE 1: typ(a) = W and typ(b) = W

G' is SC-consistent.

(Take an sc-order for G" and add a and b at the end)

# Proof outline (4)

CASE 2:  $typ(a) \in \{R, RMW\}$  and typ(b) = W

► There exists a' ~ a (a and a' are identical except for the read value, and a' may be a read if a is an RMW) such that some G<sub>a</sub> ∈ Add(G", a') is SC-consistent. (read from the last write to x in the sc-order for G")

• Let 
$$G_{ab} \in \operatorname{Add}(G_a, b)$$
.

•  $G_{ab}$  is SC-consistent and  $(po \cup rf)$ -racy.

# Proof outline (5)

CASE 3: typ(a) = R and typ(b) = RMW

- Let  $G_b \triangleq G' \setminus \{a\}$ .
- ▶ *G<sub>b</sub>* is SC-consistent. (why?)
- *b* is the  $(po \cup rf)^+$ -maximal write to *x* in  $G_b$ .
- There exists a' ~ a (a and a' are identical except for the read value) such that some G<sub>ba</sub> ∈ Add(G<sub>b</sub>, a') is SC-consistent and (b, a') ∉ G<sub>ba</sub>.rf.
  (mod from the (ne | | nf)<sup>+</sup> maximal write to x in C'')

(read from the  $(po \cup rf)^+$ -maximal write to x in G'')

•  $G_{ba}$  is (po  $\cup$  rf)-racy.

## Basic properties of program executions

#### What properties did we use?

- ▶ (po ∪ rf)-acyclicity
- RA-consistency is  $(po \cup rf)$ -prefix closed
- Receptiveness (changing the value of a final read)

Can we actually write useful programs that are not racy under SC?

► Not really...

 $\frac{\operatorname{lock}(l):}{r:=0} \qquad \qquad \frac{\operatorname{unlock}(l):}{l:=0}$ while  $\neg r$  do  $r := \operatorname{CAS}(l,0,1)$ 

- Formally, a lock induces races between the failed lock acquisition attempts and the RMW's/writes to the lock location.
- However, it suffices to consider only executions of the program in which lock acquisitions never fail (why?).
- ► All successful lock acquisitions and lock releases are totally ordered by (po∪rf)<sup>+</sup>.
- ▶ In some models (*e.g.*, full C11), locks are also primitives.

- Triangular race freedom for TSO. (Owens, ECOOP 2010)
- SC fences between every two racy accesses.

Suppose that we change the definition of an *R*-race and require also that  $R \in \{typ(a), typ(b)\}$  (that is, *R*-concurrent writes are not considered racy).

- Does RA satisfy the corresponding DRF-property?
- Does TSO satisfy the corresponding DRF-property?

Let RC11 be the simplified C11 model strengthened with (po  $\cup\,\mathtt{rf})$  acyclicity.

Let *P* be a program without RMW's. Suppose that in every RA-consistent execution graph, which is a  $(po \cup rf)^+$ -prefix of an execution graph of *P*, there are no two events *a*, *b* that  $(po \cup rf)$ -race and satisfy  $\mathbf{rlx} \in \{mod(a), mod(b)\}$ .

- Show that the outcomes of P under RC11 are the same as under RA.
- Conclude that RC11 satisfies the DRF-property.
- What happens if P contains RMW's?