

Concurrent separation logic

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Sequential separation logic

- ▶ Separating conjunction
- ▶ Fault-avoiding interpretation of SL triples

Concurrent separation logic (CSL)

- ▶ Resource invariants
- ▶ Ownership transfer

Soundness of CSL over SC

- ▶ Interpretation of CSL triples

Separation in Hoare logic: two important rules

Rule of constancy

$$\frac{\{P\} \; c \; \{Q\} \quad fv(R) \cap wr(C) = \emptyset}{\{P \wedge R\} \; c \; \{Q \wedge R\}}$$

Disjoint parallel composition

$$\frac{\begin{array}{ll} \{P_1\} \; c_1 \; \{Q_1\} & fv(P_1, c_1, Q_1) \cap wr(c_2) = \emptyset \\ \{P_2\} \; c_2 \; \{Q_2\} & fv(P_2, c_2, Q_2) \cap wr(c_1) = \emptyset \end{array}}{\{P_1 \wedge P_2\} \; c_1 \| c_2 \; \{Q_1 \wedge Q_2\}}$$

What about programs with pointers?

Points-to assertions

SL provides a convenient syntax for describing the dynamically allocated memory (aka “heap”).



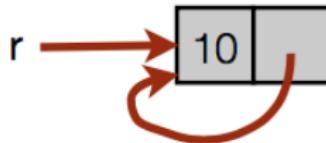
$$p \mapsto 5$$

$$\text{heap}(p) = 5$$



$$q \mapsto 5, \text{nil}$$

$$\begin{aligned} \text{heap}(q) &= 5 \wedge \\ \text{heap}(q + 1) &= \text{nil} \end{aligned}$$



$$r \mapsto 10, r$$

$$\begin{aligned} \text{heap}(r) &= 10 \wedge \\ \text{heap}(r + 1) &= r \end{aligned}$$

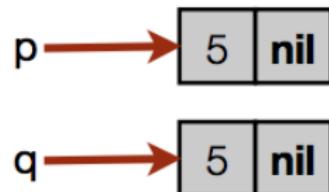
Two types of conjunction

Classical conjunction:

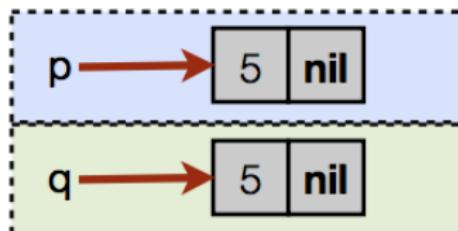


$$p \mapsto 5, \text{nil} \wedge q \mapsto 5, \text{nil}$$
$$p \mapsto 5, \text{nil} \wedge p = q$$

Separating conjunction:



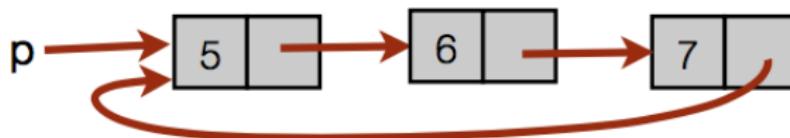
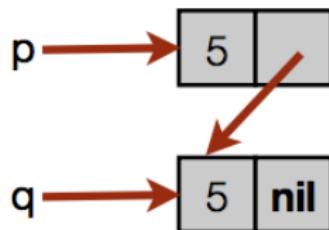
$$p \mapsto 5, \text{nil} * q \mapsto 5, \text{nil}$$



Split the heap in two parts:
one satisfying $p \mapsto 5, \text{nil}$ and
the other satisfying $q \mapsto 5, \text{nil}$.

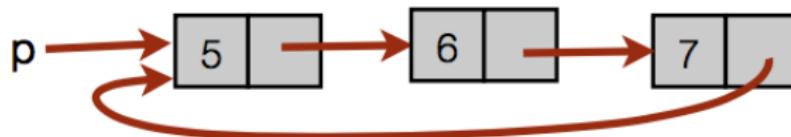
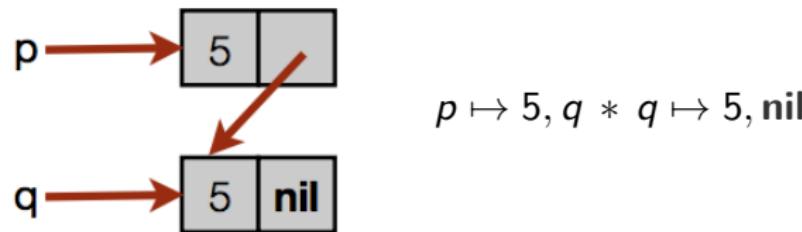
Quick quiz

Use a SL assertion to describe the following pictures:



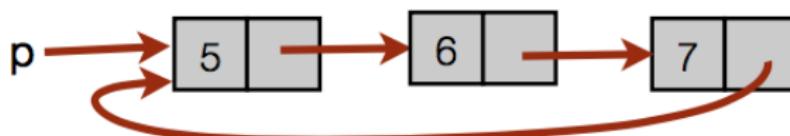
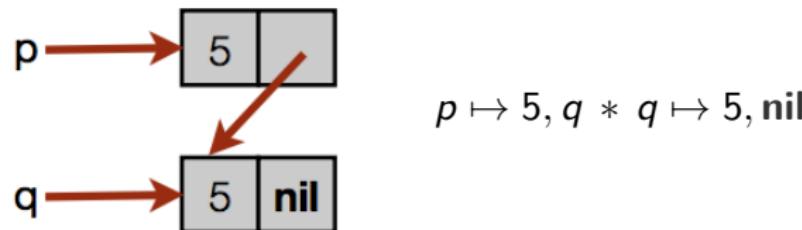
Quick quiz

Use a SL assertion to describe the following pictures:



Quick quiz

Use a SL assertion to describe the following pictures:


$$\exists q, r. \ p \mapsto 5, q * q \mapsto 6, r * r \mapsto 7, p$$

Separation logic

Key concept of *ownership* :

- ▶ Resourceful reading of Hoare triples, $\{P\} \; c \; \{Q\}$
- ▶ To access a non-atomic location, you must own it:

$$\begin{array}{l} \{ \text{emp} \} \; a := \text{alloc} \; \{ a \mapsto _ \} \\ \{ x \mapsto v \} \quad a := [x] \quad \{ x \mapsto v \wedge a = v \} \\ \{ x \mapsto v \} \quad [x] := v' \quad \{ x \mapsto v' \} \end{array}$$

- ▶ Frame rule:

$$\frac{\{P\} \; c \; \{Q\} \quad \text{fv}(R) \cap \text{wr}(C) = \emptyset}{\{P * R\} \; c \; \{Q * R\}}$$

- ▶ Disjoint parallelism:

$$\frac{\begin{array}{ll} \{P_1\} \; c_1 \; \{Q_1\} & \text{fv}(P_1, c_1, Q_1) \cap \text{wr}(c_2) = \emptyset \\ \{P_2\} \; c_2 \; \{Q_2\} & \text{fv}(P_2, c_2, Q_2) \cap \text{wr}(c_1) = \emptyset \end{array}}{\{P_1 * P_2\} \; c_1 \| c_2 \; \{Q_1 * Q_2\}}$$

Separation logic: Disjoint parallelism

$$\frac{\left\{x \mapsto 0 * y \mapsto 0\right\}}{a := [x]; \quad \parallel \quad b := [y]; \\ [x] := a + 1; \quad \parallel \quad [y] := b + 1; \\ \left\{x \mapsto 1 * y \mapsto 1\right\}}$$

Threads mind their own business!

Separation logic: Disjoint parallelism

$$\frac{\begin{array}{c} \{x \mapsto 0\} \\ a := [x]; \\ \{x \mapsto 0 \wedge a = 0\} \\ [x] := a + 1; \\ \{x \mapsto 1\} \end{array} \parallel \begin{array}{c} \{y \mapsto 0\} \\ b := [y]; \\ \{y \mapsto 0 \wedge b = 0\} \\ [y] := b + 1; \\ \{y \mapsto 1\} \end{array}}{\{x \mapsto 1 * y \mapsto 1\}}$$

Simple programs are easy to verify!

Sequential language

$$E ::= x \mid n \mid E + E \mid E - E \mid \dots$$
$$B ::= B \wedge B \mid \neg B \mid E = E \mid E \leq E \mid \dots$$
$$\begin{aligned} C ::= & \textbf{skip} \mid x := E \mid x := [E] \mid [E] := E \mid x := \textbf{alloc}(E) \mid \textbf{free}(E) \\ & \mid C_1; C_2 \mid \textbf{if } B \text{ then } C_1 \text{ else } C_2 \mid \textbf{while } B \text{ do } C \end{aligned}$$

Small-step operational semantics:

$$(C, s, h) \rightarrow (C', s', h') \quad s : \text{Stack} \triangleq \text{VarName} \rightarrow \text{Val}$$

$$(C, s, h) \rightarrow \text{abort} \quad h : \text{Heap} \triangleq \text{Loc} \rightarrow \text{Val}$$

Rules for sequential composition:

$$(\textbf{skip}; C, s, h) \rightarrow (C, s, h) \quad \frac{(C_1, s, h) \rightarrow (C'_1, s', h')}{(C_1; C_2, s, h) \rightarrow (C'_1; C_2, s', h')}$$

Parallel composition

$$C ::= \dots \mid C_1 \| C_2$$

- ▶ Interleaving semantics:

$$\frac{(C_1, s, h) \rightarrow (C'_1, s', h')}{(C_1 \| C_2, s, h) \rightarrow (C'_1 \| C_2, s', h')}$$

$$\frac{(C_2, s, h) \rightarrow (C'_2, s', h')}{(C_1 \| C_2, s, h) \rightarrow (C_1 \| C'_2, s', h')}$$

- ▶ Abort semantics:

$$\frac{(C_1, s, h) \rightarrow \text{abort}}{(C_1 \| C_2, s, h) \rightarrow \text{abort}}$$

$$\frac{(C_2, s, h) \rightarrow \text{abort}}{(C_1 \| C_2, s, h) \rightarrow \text{abort}}$$

- ▶ Termination:

$$(\text{skip} \| \text{skip}, s, h) \rightarrow (\text{skip}, s, h)$$

Atomic blocks

$$C ::= \dots \mid \text{atomic } C$$

Atomic blocks execute in one step

$$\frac{(C, s, h) \rightarrow^* (\text{skip}, s', h')}{(\text{atomic } C, s, h) \rightarrow (\text{skip}, s', h')}$$

$$\frac{(C, s, h) \rightarrow^* \text{abort}}{(\text{atomic } C, s, h) \rightarrow \text{abort}}$$

Note

Normally, we also need a rule for non-terminating atomic blocks:

$$\frac{(C, s, h) \rightarrow^\omega}{(\text{atomic } C, s, h) \rightarrow (\text{atomic } C, s, h)}$$

Multiple resources

- ▶ Lock declarations & conditional critical regions (CCRs)

$$C ::= \dots \mid \text{resource } r \text{ in } C \mid \text{with } r \text{ when } B \text{ do } C \\ \mid \text{within } r \text{ do } C$$

- ▶ Enter a CCR

$$\frac{\llbracket B \rrbracket(s)}{(\text{with } r \text{ when } B \text{ do } C, s, h) \rightarrow (\text{within } r \text{ do } C, s, h)}$$

- ▶ Execute body of a CCR

$$\frac{(C, s, h) \rightarrow (C', s', h') \quad r \notin \text{Locked}(C')}{(\text{within } r \text{ do } C, s, h) \rightarrow (\text{within } r \text{ do } C', s', h')}$$

- ▶ Exit the CCR

$$(\text{within } r \text{ do skip}, s, h) \rightarrow (\text{skip}, s, h)$$

Operational semantics for parallel composition

$$\frac{(C_1, s, h) \rightarrow (C'_1, s', h') \\ \textcolor{red}{Locked}(C'_1) \cap \textcolor{red}{Locked}(C_2) = \emptyset}{(C_1 \| C_2, s, h) \rightarrow (C'_1 \| C_2, s', h')}$$

$$\frac{(C_2, s, h) \rightarrow (C'_2, s', h') \\ \textcolor{red}{Locked}(C_1) \cap \textcolor{red}{Locked}(C'_2) = \emptyset}{(C_1 \| C_2, s, h) \rightarrow (C_1 \| C'_2, s', h')}$$

where

$$\textcolor{black}{Locked}(C) \triangleq \{r \mid \exists C'. (\textbf{within } r \textbf{ do } C') \text{ is a subterm of } C\}$$

Ensures that commands are well-formed:

$$wf(\textbf{skip}) \triangleq true$$

$$wf(C_1; C_2) \triangleq wf(C_1) \wedge wf(C_2) \wedge (\textcolor{black}{Locked}(C_2) = \emptyset)$$

$$wf(C_1 \| C_2) \triangleq wf(C_1) \wedge wf(C_2) \wedge (\textcolor{black}{Locked}(C_1) \cap \textcolor{black}{Locked}(C_2) = \emptyset)$$

$$wf(\textbf{with } r \textbf{ when } B \textbf{ do } C) \triangleq wf(C) \wedge (\textcolor{black}{Locked}(C) = \emptyset)$$

$$wf(\textbf{within } r \textbf{ do } C) \triangleq wf(C) \wedge r \notin \textcolor{black}{Locked}(C)$$

Some proof rules

$$\frac{\Gamma \vdash \{P_1\} C_1 \{Q_1\} \quad fv(\Gamma, P_1, C_1, Q_1) \cap wr(C_2) = \emptyset}{\Gamma \vdash \{P_2\} C_2 \{Q_2\} \quad fv(\Gamma, P_2, C_2, Q_2) \cap wr(C_1) = \emptyset}{\Gamma \vdash \{P_1 * P_2\} C_1 \| C_2 \{Q_1 * Q_2\}} \quad (\text{PAR})$$

$$\frac{\Gamma \vdash \{(P * J) \wedge B\} C \{Q * J\}}{\Gamma, r : J \vdash \{P\} \text{ with } r \text{ when } B \text{ do } C \{Q\}} \quad (\text{WITH})$$

$$\frac{\Gamma, r : J \vdash \{P\} C \{Q\} \quad fv(J) \cap wr(C) = \emptyset}{\Gamma \vdash \{P * J\} \text{ resource } r \text{ in } C \{Q * J\}} \quad (\text{RES})$$

$$\frac{\Gamma \vdash \{P\} C \{Q\} \quad fv(R) \cap wr(C) = \emptyset}{\Gamma \vdash \{P * R\} C \{Q * R\}} \quad (\text{FRAME})$$

Note

The rules have draconian variable side-conditions.

Proof rules for atomic blocks

$$\frac{J \vdash \{P_1\} C_1 \{Q_1\} \quad fV(J, P_1, C_1, Q_1) \cap wr(C_2) = \emptyset}{J \vdash \{P_2\} C_2 \{Q_2\} \quad fV(J, P_2, C_2, Q_2) \cap wr(C_1) = \emptyset}{J \vdash \{P_1 * P_2\} C_1 \| C_2 \{Q_1 * Q_2\}} \text{ (PAR)}$$

$$\frac{\mathbf{emp} \vdash \{P * J\} C \{Q * J\}}{J \vdash \{P\} \mathbf{atomic} C \{Q\}} \text{ (ATOM)}$$

$$\frac{J * R \vdash \{P\} C \{Q\} \quad fV(R) \cap wr(C) = \emptyset}{J \vdash \{P * R\} C \{Q * R\}} \text{ (SHARE)}$$

$$\frac{J \vdash \{P\} C \{Q\} \quad fV(R) \cap wr(C) = \emptyset}{J \vdash \{P * R\} C \{Q * R\}} \text{ (FRAME)}$$

Hoare triples (partial correctness)

$$\models \{P\} C \{Q\}$$

if and only if

$$\forall s h s' h'. s, h \models P \wedge (C, s, h) \rightarrow^* (\text{skip}, s', h') \Rightarrow s', h' \models Q$$

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if and only if

$$\begin{aligned} \forall s h. s, h \models P \Rightarrow \\ (\forall s' h'. (C, s, h) \rightarrow^* (\text{skip}, s', h') \Rightarrow s', h' \models Q) \end{aligned}$$

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if and only if

$$\forall s h. s, h \models P \Rightarrow (\textcolor{red}{\forall m.} \forall s' h'. (C, s, h) \rightarrow^m (\text{skip}, s', h') \Rightarrow s', h' \models Q)$$

Hoare triples (partial correctness)

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$$\forall s h. s, h \models P \Rightarrow (\forall s' h'. (C, s, h) \rightarrow^* (\text{skip}, s', h') \Rightarrow s', h' \models Q)$$

if and only if

$$\forall s h. s, h \models P \Rightarrow (\forall m. \forall s' h'. (C, s, h) \xrightarrow{m} (\text{skip}, s', h') \Rightarrow s', h' \models Q)$$

if and only if

$$\forall s h n. s, h \models P \Rightarrow (\forall m < n. \forall s' h'. (C, s, h) \xrightarrow{m} (\text{skip}, s', h') \Rightarrow s', h' \models Q)$$

Configuration safety

$$\models \{P\} C \{Q\} \text{ iff } \forall s h n. \ s, h \models P \Rightarrow \text{safe}_n(C, s, h, Q)$$

where

$$\begin{aligned} \text{safe}_n(C, s, h, Q) &\triangleq \\ (\forall m < n. \ \forall s' h'. \ (C, s, h) \rightarrow^m (\text{skip}, s', h') \Rightarrow s', h' \models Q) \end{aligned}$$

As an inductive definition:

$$\text{safe}_0(C, s, h, Q) = \text{true}$$

$$\begin{aligned} \text{safe}_{n+1}(C, s, h, Q) &= \\ (C = \text{skip} \Rightarrow s, h \models Q) \wedge &(\forall C' s' h'. \ (C, s, h) \rightarrow (C', s', h') \\ &\Rightarrow \text{safe}_n(C', s', h', Q)) \end{aligned}$$

Configuration safety

$\models \{P\} C \{Q\}$ iff $\forall s h n. s, h \models P \Rightarrow \text{safe}_n(C, s, h, Q)$

$\text{safe}_0(C, s, h, Q) \triangleq \text{true}$

$\text{safe}_{n+1}(C, s, h, Q) \triangleq$
 $(C = \text{skip} \Rightarrow s, h \models Q)$
 $\wedge (\forall C' s' h'. (C, s, h) \rightarrow (C', s', h')$
 $\Rightarrow \text{safe}_n(C', s', h', Q))$

Fault-avoidance

$\models \{P\} C \{Q\}$ iff $\forall s h n. s, h \models P \Rightarrow \text{safe}_n(C, s, h, Q)$

$\text{safe}_0(C, s, h, Q) \triangleq \text{true}$

$\text{safe}_{n+1}(C, s, h, Q) \triangleq$
 $(C = \text{skip} \Rightarrow s, h \models Q)$

$\wedge (\neg(C, s, h) \rightarrow \text{abort})$

$\wedge (\forall C' s' h'. (C, s, h) \rightarrow (C', s', h')$
 $\Rightarrow \text{safe}_n(C', s', h', Q))$

“Well-specified programs don't go wrong.”

“Bake in” the frame rule

$\models \{P\} C \{Q\}$ iff $\forall s h n. s, h \models P \Rightarrow \text{safe}_n(C, s, h, Q)$

$\text{safe}_0(C, s, h, Q) \triangleq \text{true}$

$\text{safe}_{n+1}(C, s, h, Q) \triangleq$

$(C = \text{skip} \Rightarrow s, h \models Q)$

$\wedge (\forall h_F. \neg(C, s, h \uplus h_F) \rightarrow \text{abort})$

$\wedge (\forall h_F C' s' h'. (C, s, h \uplus h_F) \rightarrow (C', s', h')$

$\Rightarrow \exists h''. h' = h'' \uplus h_F \wedge \text{safe}_n(C', s', h'', Q))$

Note

- ▶ No need for safety monotonicity & frame property.
- ▶ The same definition works for permissions.
(where \uplus becomes the addition of permission-heaps)

Atomic blocks

$\textcolor{red}{J} \models \{P\} \ C \ \{Q\}$ iff $\forall s \ h \ n. \ s, h \models P \Rightarrow \text{safe}_n(C, s, h, \textcolor{red}{J}, Q)$

$\text{safe}_0(C, s, h, \textcolor{red}{J}, Q) \triangleq \text{true}$

$\text{safe}_{n+1}(C, s, h, \textcolor{red}{J}, Q) \triangleq$

$(C = \text{skip} \Rightarrow s, h \models Q)$

$\wedge (\forall h_J \ h_F. \ s, h_J \models J \Rightarrow \neg(C, s, h \uplus h_J \uplus h_F) \rightarrow \text{abort})$

$\wedge (\forall h_J \ h_F \ C' \ s' \ h'. \ (C, s, h \uplus h_J \uplus h_F) \rightarrow (C', s', h'))$

$\wedge s, h_J \models J$

$\Rightarrow \exists h'' \ h'_J. \ h' = h'' \uplus h'_J \uplus h_F$

$\wedge s, h'_J \models J$

$\wedge \text{safe}_n(C', s', h'', \textcolor{red}{J}, Q))$

- ▶ Add heap h_J satisfying the resource invariant, J .
- ▶ Resource invariant must be re-established in h'_F .

Multiple resources

$$\Gamma \models \{P\} C \{Q\} \text{ iff } \forall s h n. s, h \models P \Rightarrow \text{safe}_n(C, s, h, \Gamma, Q)$$

$$\text{safe}_0(C, s, h, \Gamma, Q) \triangleq \text{true}$$

$$\text{safe}_{n+1}(C, s, h, \Gamma, Q) \triangleq$$

$$(C = \text{skip} \Rightarrow s, h \models Q)$$

$$\wedge (\forall h_F. \neg(C, s, h \uplus h_F) \rightarrow \text{abort})$$

$$\wedge (\forall h_J h_F C' s' h'. (C, s, h \uplus h_J \uplus h_F) \rightarrow (C', s', h'))$$

$$\wedge s, h_J \models \circledast_{r \in \text{Locked}(C') \setminus \text{Locked}(C)} \Gamma(r)$$

$$\Rightarrow \exists h'' h'_J. h' = h'' \uplus h'_J \uplus h_F$$

$$\wedge s, h'_J \models \circledast_{r \in \text{Locked}(C) \setminus \text{Locked}(C')} \Gamma(r)$$

$$\wedge \text{safe}_n(C', s', h'', \Gamma, Q))$$

- ▶ Assume res. invariant satisfied only for acquired locks (h_J).
- ▶ Ensure res. invariant satisfied for released locks (h'_J).

Further reading

- ▶ Separation logic: A logic for shared mutable data structures.
John C. Reynolds: LICS 2002: 55-74
- ▶ Resources, concurrency, and local reasoning.
Peter W. O'Hearn, TCS 375(1-3): 271-307 (2007)
- ▶ Concurrent separation logic and operational semantics.
Viktor Vafeiadis, ENTCS 276: 335-351 (2011)